Amendments to the Specification:

Please amend the title of the Application at line 1 of page 2 of the Application as follows: ~~Renaming for Register with Multiple Bit Fields~~<u>Apparatus and Method Using Different Size Rename Registers for Partial-bit and Bulk-bit Writes</u>.

Similarly, please amend the title of the Application preceding the Abstract at line 1 of page 33 of the Application as follows: ~~Renaming for Register with Multiple Bit Fields~~<u>Apparatus and Method Using Different Size Rename Registers for Partial-bit and Bulk-bit Writes</u>.

The Office Action requests that Applicants amend the penultimate line of entry 5 in Table 1 to correct "appropnate" to read "appropriate." However, this error was not in the Application as originally filed (see scanned copy of original Application in PAIR records) but instead must have been introduced by the Patent Office during the publication process. The penultimate line of entry 5 of Table 1 should read:

5.	mov pr = r2, *mask*	Bulk-bit instruction. Any one, several, or all of the PR bits may be written. More than two contiguous or non-contiguous bits may be written. Those PR bits indicated by *mask* are written with value in source register r2 at the ~~appropnate~~ <u>appropriate</u> position.